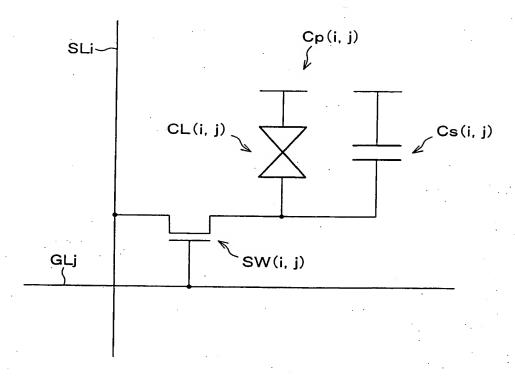


FIG. 2



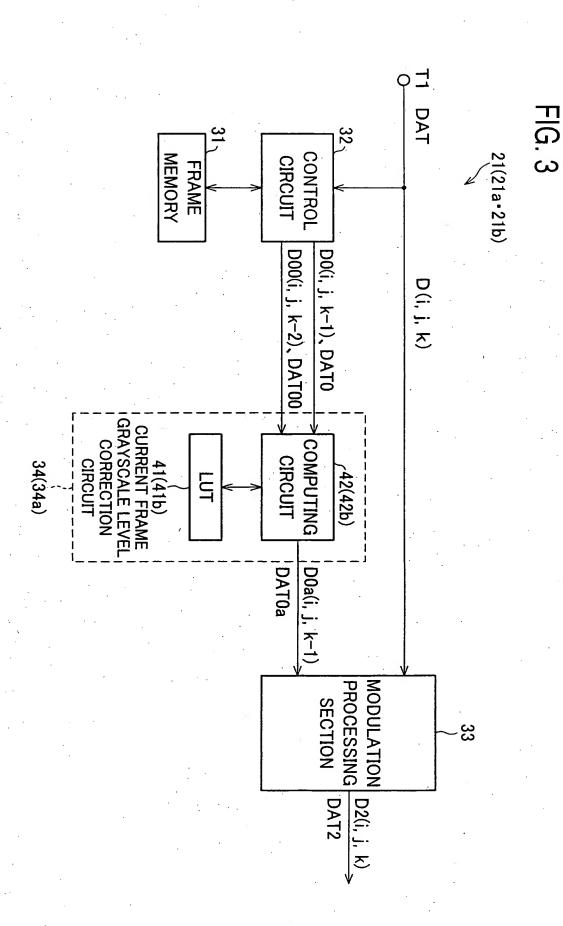


FIG. 4

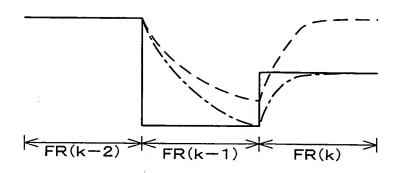


FIG. 5

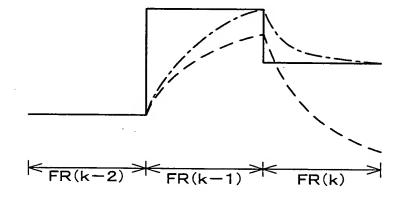
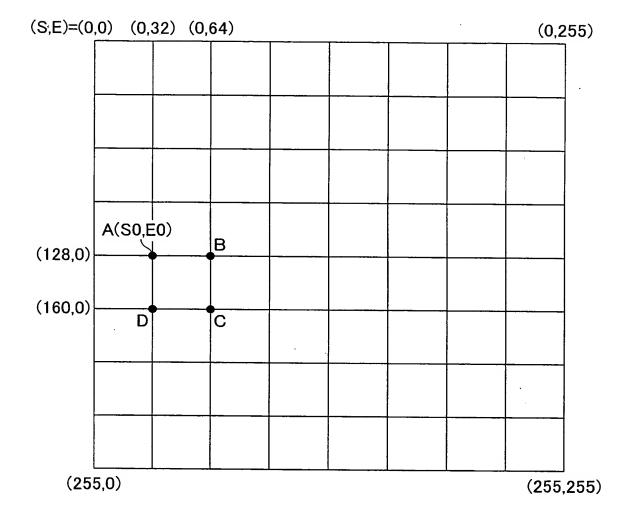


FIG. 6



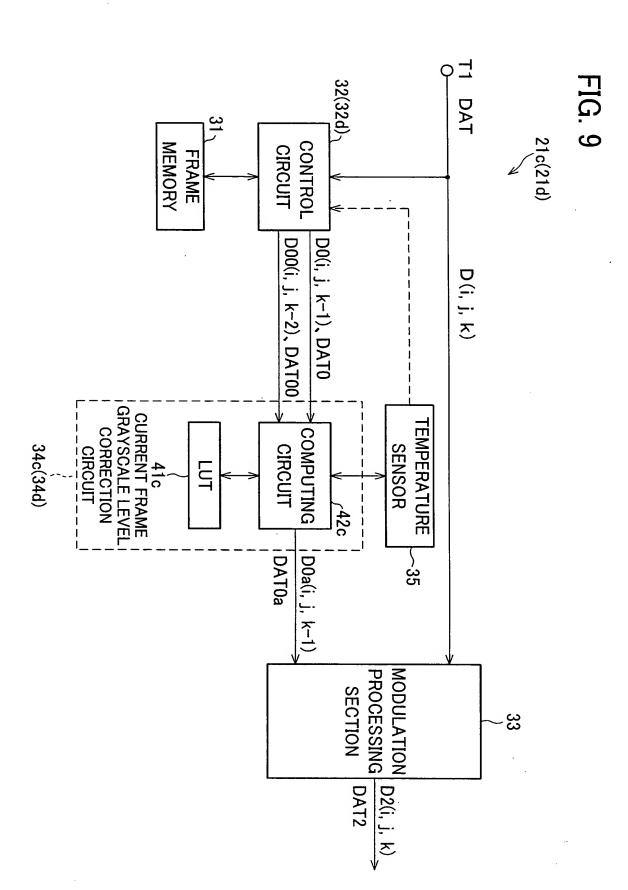
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FIG. 7

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	255	224	192	160	128	96	64	32	0	SE
	194	192	176	155	123	93	63	35	0	0
	239	223	189	157	125	94	63	32	26	32
α2	246	224	188	157	126	95	64	42	42	64
	249	224	190	159	127	96	65	56	56	96
	251	223	190	158	128	96	64	64	64	128
	253	225	191	160	128	97	76	76	76	160
	254	224	192	160	128	99	90	90	90	192
	254	224	191	160	129	112	112	112	112	224
18	255	223	192	159	134	134	134	134	134	255
				α1						

FIG. 8

0	32	64	96	128	160	192	224	255	
. 0	32	64	96	128	160	176	192	194	
26	32	64	96	128	160	192	224	239	
42	42	64	96	128	160	192	224	255	α
56	56	64	96	128	160	192	224	255	
64	64	64	96	128	160	192	224	255	
76	76	76	96	128	160	192	224	255	
90	90	90	96	128	160	192	224	255	,
112	112	112	112	128	160	192	224	255	
134	134	134	134	134	160	192	224	255	
-		6			α1		1		
	0 26 42 56 64 76 90 112	0 32 26 32 42 42 56 56 64 64 76 76 90 90 112 112	0 32 64 26 32 64 42 42 64 56 56 64 64 64 64 76 76 76 90 90 90 112 112 112	0 32 64 96 26 32 64 96 42 42 64 96 56 56 64 96 64 64 64 96 76 76 76 96 90 90 90 96 112 112 112 112	0 32 64 96 128 26 32 64 96 128 42 42 64 96 128 56 56 64 96 128 64 64 64 96 128 76 76 76 96 128 90 90 90 96 128 112 112 112 112 128	0 32 64 96 128 160 26 32 64 96 128 160 42 42 64 96 128 160 56 56 64 96 128 160 64 64 64 96 128 160 76 76 76 96 128 160 90 90 90 96 128 160 112 112 112 112 128 160 134 134 134 134 134 160	0 32 64 96 128 160 176 26 32 64 96 128 160 192 42 42 64 96 128 160 192 56 56 64 96 128 160 192 64 64 64 96 128 160 192 76 76 76 96 128 160 192 90 90 90 96 128 160 192 112 112 112 112 128 160 192 134 134 134 134 134 160 192	0 32 64 96 128 160 176 192 26 32 64 96 128 160 192 224 42 42 64 96 128 160 192 224 56 56 64 96 128 160 192 224 64 64 64 96 128 160 192 224 76 76 76 96 128 160 192 224 90 90 90 96 128 160 192 224 112 112 112 128 160 192 224 134 134 134 134 134 160 192 224	0 32 64 96 128 160 176 192 194 26 32 64 96 128 160 192 224 239 42 42 64 96 128 160 192 224 255 56 56 64 96 128 160 192 224 255 64 64 64 96 128 160 192 224 255 76 76 76 96 128 160 192 224 255 90 90 90 96 128 160 192 224 255 112 112 112 112 128 160 192 224 255 134 134 134 134 134 160 192 224 255



T1 DAT FIG. 10 32(32f)-<u>د</u> ر 21e(21f) CONTROL CIRCUIT FRAME MEMORY D00(i, j, k-2), DAT00i D0(i, j, k-1), DAT0 D(i, j, k) 41e
CURRENT FRAME
GRAYSCALE LEVEL
CORRECTION
CIRCUIT DETERMINING COMPUTING 34e(34f) CIRCUIT CIRCUIT TUT ,42c DAT0a D0a(i, j, k-1) >36 MODULATION D2(i, j, k) SECTION င္ယ DAT2

FIG. 11

